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APPLICATION NO.

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

(07/25/00)

(Use several sheets if necessary)

APPLICANT  
William J. Dally et al.

FILING DATE

GROUP

## U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
11/29/02	AA	4,914,429	04/03/90	Upp	340	825.8	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

JC657 U.S. PTO  
09/625802  
07/26/01

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

11/29/02	AR	Shin, Hyun J. and Hodges, David A., "A 250-Mbit/s CMOS Crosspoint Switch," IEEE Journal of Solid-State Circuits, Vol. 24, No. 2, April 1989, pp. 478-486.
	AS	Dally, William J. and Poulton, John W., "Digital Systems Engineering," Cambridge University Press, 1998, pp. 385-387.
	AT	

EXAMINER

DATE CONSIDERED

11/29/02